

PATENT ABSTRACTS OF JAPAN

(11) Publication number : 2003-259647

(43) Date of publication of application : 12.09.2003

(51)Int.Cl. H02M 5/297

(21)Application number : 2002-177866 (71)Applicant : OTIS ELEVATOR CO

(22) Date of filing : 19.06.2002 (72) Inventor : AJIGUCHI YASUHIKO

(30)Priority

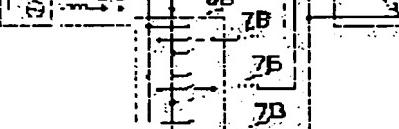
Priority number : 2001403131 Priority date : 27.12.2001 Priority country : JP

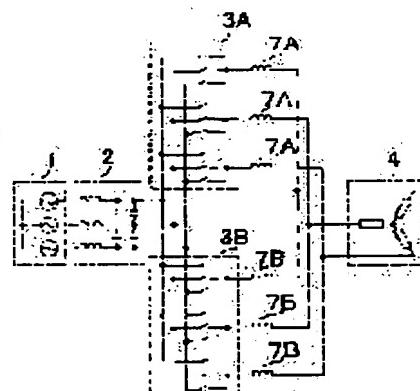
(54) MULTIPLEX PWM CYCLOCONVERTER

(57) Abstract:

PROBLEM TO BE SOLVED: To solve the problem of a prior art PWM cycloconverter that, since an open circuit of an output terminal and a short circuit on an input side must be avoided, it is theoretically difficult to constitute a high current stack including a snubber, and thereby it is difficult to manufacture a high capacity converter.

SOLUTION: The multiplex PWM cycloconverter is arranged such that output terminals of respective phases of p unit converters in an n input m output PWM cycloconverter comprising $n \times m$ bidirectional switches (n, m and p are integers of 2 or larger, respectively) are connected in parallel through reactors to have m outputs, as a whole. Each output phase of each unit converter is provided with a current sensor in order to detect a current and an output voltage command for each unit converter is corrected depending on the imbalance of the current. According to the arrangement, a high capacity converter can be obtained and since a capacitor is not required at a DC link part, the volume and cost of the converter can be reduced.





LEGAL STATUS

[Date of request for examination] 15.06.2005

[Date of sending the examiner's decision of rejection]

[Kind of final disposal of application other than the examiner's decision of rejection or application converted registration]

[Date of final disposal for application]

[Patent number]

[Date of registration]

[Number of appeal against examiner's decision of rejection]

[Date of requesting appeal against examiner's decision of rejection]

[Date of extinction of right]